

LVDS Receiver

Features

- Wide frequency range:
Input: 20 – 150 MHz
Output: 20 – 150 MHz
- Power-Down Mode
- Supports VGA, SVGA, XGA, SXGA, SXGA+ and QXGA
- Up to 10.5 Gbit/s bandwidth
- PLL requires no external components
- 8- or 10-bit mode

Applications

- LCD Displays
- Laptop PC

Block Diagram

