

# LVDS Transmitter

## Features

- Wide frequency range:  
Input: 20 – 150 MHz  
Output: 20 – 150 MHz
- Power-Down Mode
- Supports VGA, SVGA, XGA, SXGA, SXGA+ and QXGA
- On-chip Input Jitter Filtering
- Up to 10.5 Gbit/s bandwidth
- 200 mV swing LVDS devices for low EMI
- PLL requires no external components
- Selectable 8- or 10-bit mode

## Applications

- LCD Displays
- HDTV
- Projectors

## General Description

The transmitter is designed to support Single link transmission between Host and Flat Panel Display up to 1080p resolutions, Dual Link transmission between Host and Flat Panel Display up to QXGA resolutions. The transmitter converts 70 bits of CMOS data into LVDS (Low Voltage Differential Signaling) data streams. Using a 150MHz clock, the data throughput is 10.5Gbps.

## Block Diagram

